UNITED STATES DEPARTMENT OF COMMERCE Patent and Trademark Office ASSISTANT SECRETARY OF COMMERCE AND COMMISSIONER OF PATENTS AND TRADEMARKS

P.O. Box 1450 Alexandria, VA 22313-1450

August 6, 2007

FREESCALE SEMICONDUCTOR, INC. LAW DEPARTMENT 7700 WEST PARMER LANE MD:TX32/PL02

AUSTIN TX 78729

Patent No.

: 7,105,430 B2

Inventor(s)

: Marius K. Orlowski, et al.

Issued

: September 12, 2006

For

METHOD FOR FORMING A

SEMICONDUCTOR DEVICE HAVING A NOTCHED CONTROL ELECTRODE AND

STRUCTURE THEREOF

Doc. No.

SC13240TP

To Whom It May Concern:

The Certificate of Correction issued on <u>August 7, 2007</u>, issued in error, in that error(s) was made in identifying the patent number and/or keying text/corrections, i.e.:

The issue date in the heading of cofc has been incorrectly entered in the heading of the issued cofc. A superseding cofc will be issued to correct the patent issue date to read --September 12, 2006-.

Therefore, a certificate of correction will be issued to correct (supersede) the Certificate of Correction containing error(s), made during preparation of the Certificate of Correction, as noted above.

No further response is required, from applicants (attorney). However, errors discovered by attorney, other than as noted and described above, should be noted on *a copy* of the Certificate of Correction that was issued in error, accompanied by a signed transmittal letter and submitted directed to this Branch.

Antonio Johnson (703) 308-9390 ext. 111 For Cecelia Newman, Supervisor Decisions & Certificates of Correction Branch (703) 305-8309 / 703-308-9390 ext. 102 cbn